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**APPLICANTS**

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**\*\* CONTINUING DATA \*\*\*\*\****NONE 9/6***\*\* FOREIGN APPLICATIONS \*\*\*\*\***JAPAN 2002-245377 08/26/2002 *2/11***IF REQUIRED, FOREIGN FILING LICENSE GRANTED**

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Foreign Priority claimed	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no	STATE OR COUNTRY	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after allowance	JAPAN	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged	<i>William M. Brewster</i> Examiner's Signature	Initials	16	18	1

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**TITLE**

Semiconductor apparatus fabrication method

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